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Title of Invention:	Data access control method for tamper resistant microprocessor using cache memory
First Named Inventor/Applicant Name:	Kensaku Yamaguchi
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		247558usAmend.pdf	195560	yes	6
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	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Miscellaneous Incoming Letter	1	1
	Applicant Arguments/Remarks Made in an Amendment	2	6
Warnings:			
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